CLAIMS:

1.   A method for storing at least one software specification for a plurality of processors in a multi-processor system, comprising the steps of:
storing each unique software specification in a memory; and
storing a pointer for each of said plurality of processors, each of said pointers identifying a location of said memory storing a software specification associated with said processor.

2.   The method of claim 1, further comprising the step of retrieving a pointer to a corresponding program software specification for a given processor.

3.   The method of claim 2, further comprising the step of retrieving said software specification identified by said pointer.

4.   The method of claim 3, further comprising the step of loading said retrieved software specification into said given processor.

5.   The method of claim 1, wherein each unique software specification has a uniform length.

6.   The method of claim 1, wherein one or more of said pointers are encoded using run length encoding techniques.

7.   The method of claim 1, wherein at least one of said unique software specifications is stored in a compressed format using a keyword that indicates that an associated operational code should be repeated.

8.   A multi-processor system, comprising:
a plurality of processors; and
a memory coupled to said plurality of processors, said memory including:
a first region for storing one or more unique software specifications and

a second region for storing a pointer for each of said plurality of processors, each of said pointers identifying a location of said memory storing a software specification associated with said processor.

9.   The multi-processor system of claim 8, wherein at least one of said plurality of processors is configured to retrieve a pointer to a corresponding program software specification for a given processor.

10.   The multi-processor system of claim 9, wherein at least one of said plurality of processors is configured to retrieve said software specification identified by said pointer.

11.   The multi-processor system of claim 10, wherein at least one of said plurality of processors is configured to load said retrieved software specification into said given processor.

12.   The multi-processor system of claim 8, wherein each unique software specification has a uniform length.

13.   The multi-processor system of claim 8, wherein one or more of said pointers are encoded using run length encoding techniques.

14.   The multi-processor system of claim 8, wherein at least one of said processors recognizes a keyword that indicates that an associated operational code in one of said software specifications should be repeated.

15.   A method for loading a software specification into a processor in a multi-processor system, comprising the steps of:
retrieving a pointer associated with said processor that identifies a corresponding software specification in a memory;
retrieving said corresponding software specification identified by said pointer; and
loading said retrieved software specification into said processor.

16.   The method of claim 15, wherein said software specification has a predefined length.

17.   The method of claim 15, wherein said pointer is encoded using run length encoding techniques.

18.   The method of claim 15, wherein said software specifications is stored in a compressed format using a keyword that indicates that an associated operational code should be repeated.

19.   The method of claim 18, further comprising the step of expanding said operational code an indicated number of times.

20.   The method of claim 18, wherein said software specification is stored only once in said memory.